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Attorney's Docket No.: 7977-052001 / US3053

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani et al. Art Unit : 1765
Serial No.: 08/690,747 Examiner : Robert Kunemund
Filed : August 1, 1996
Title: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Commissioner for Patents
Washington, D.C. 20231

RESPONSE

In response to the action mailed May 18, 2001, please consider the remarks as follows:

REMARKS

Claims 21, 25, and 37-96 are pending in the application.

Claims 21, 25, and 37 are rejected under the judicially created doctrine of obviousness-type double patenting as allegedly being unpatentable over claims 1-48 of U.S. Patent No. 6,066,518. Applicant respectfully disagrees with this rejection and requests that this rejection be held in abeyance until the claims are finally deemed allowable over the prior art cited by the Examiner.

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

October 17, 2001

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